



## yieldWerx helps Silicon Image save millions

### Background

Silicon Image is a leading provider of multimedia connectivity solutions and services for mobile, consumer electronics and PC markets. The company develops market-leading technologies and industry standards that deliver connectivity across a wide array of devices in the home, office and on the go. The company sells its semiconductor and intellectual property core products to a customer base that includes global brands in mobile, CE and PC markets. Silicon Image's advanced connectivity solutions are deployed in mobile phones, tablets, DTVs, Blu-ray Disc™ players, audio-video receivers, digital cameras, as well as desktop and notebooks.

### Yield Analysis before yieldWerx

Silicon Image's previous YMS practice was to first process the raw data in Excel and then review. This used to take them almost a week to track down simple tasks such as ATE HW related dependencies to address the root cause of yield loss. The team after spending several days on manual data aggregation and reviews could then inform the test floor about the issue - but by that time, millions of parts had been tested and shipped out. The previous tool did not give them one single-source point of access to get all their data from various locations of their fragmented supply chain. It took 2-3 days of their engineering time just to collect the data. Every

engineering function had its own set of data and analysis, resulting in enterprise wide discrepancies and a clear lag in decision making within the Product Engineering organization.

### Yield Analysis after yieldWerx

Silicon Image is shipping 200+ million parts of significant yield dependencies, and had to analyze which fabs and test houses were yielding better and which were not. Previously with their old solution they would have to dump data in an access database, and then write a script to make the lot genealogy, which would take several days to do the lot traceability detection alone. After adopting [yieldWerx](#), it now takes only a few minutes to identify and isolate yield excursions within the manufacturing process, saving up to 10x in terms of engineering time and provides an enterprise wide view of the test results for quick response and resolution.

### Financial Gains and Calculation of ROI

[yieldWerx](#) helped Silicon Image shorten time to entitled yields across their supply chain, optimizing valuable test time, enhancing equipment throughput and giving instant insight into their global manufacturing supply chain, resulting in significant improvement in operational performance, optimizing and better understanding of systematic issue that resulted in significant savings for the company.

"We totally rely on yieldWerx to drive our foundry, assembly and test vendors about yield issues that we spot in production. It used to take our Product Engineering team about 4 weeks to prepare, analyze and summarize the data from a quarterly production run, and then another 2-3 weeks to discuss with our fab, assembly and test house sub cons for DOEs. This now takes us just a few hours to prepare the same report with yieldWerx, and is accepted by our fab, assembly and test house sub cons, putting us on track to resolve yield issues and ramp our yield much faster. This translates into tremendous financial gains for the company and engineering efficiency for us."

- Baqar Tabrez ( Sr. Product Engineering Manager)